AMENDMENTS TO THE SPECIFICATION

Please replace paragraph [0014] with the following amended paragraph:

Fig.3_is a top view of the plasma panel shown in Fig.2.

Please replace paragraph [0015] with the following amended paragraph:

Fig.4_is a top view of a plasma panel according to a second preferred embodiment of the present invention.

Please replace paragraph [0016] with the following amended paragraph:

The present invention provides a plasma panel having a high luminous efficiency. Referring to Fig.2. Fig.2 is a cross-sectional diagram of a plasma panel 100 according to a first preferred embodiment of the present invention. The present invention plasma panel 100 comprises a rear plate 102, and a front plate 104 disposed parallel with and spaced apart from the rear plate 102. A plurality of electrode pairs 106 are disposed on a top surface 108 of the rear plate 102. Each electrode pair 106 comprises a positive electrode 112 and a negative electrode 114. The positive electrode 112 and the negative electrode 114 of each electrode pair 106 are spaced equally, and a discharge

gap is formed between the positive electrode 112 and the negative electrode 114 of each electrode pair 106. A dielectric layer 116 having a predefined pattern is disposed on the top surface 108 of the rear plate 102 to cover the electrode pairs 106 so as to protect and isolate the electrode pairs 106.

Please replace paragraph [0018] with the following amended paragraph:

Referring to Fig.3. Fig.3 is a top view of the plasma panel 100 shown in Fig.2. As shown in Fig.3, since the dielectric layer 116 has the predefined pattern shown in Fig.3, the dielectric layer 116 does not cover the positive electrode 112 and the negative electrode 114 levelly. Rather, the dielectric layer 116 presents in a sequence of a protrusion and an indentation. Due to the recess in the dielectric layer 116 between the adjacent positive electrode 112 and the negative electrode 114, the total area for the fluorescent layer 122 coated on the dielectric layer 116 and the top surface 108 of the rear plate 102 is effectively increased to increase the luminous efficiency of the plasma panel 100.

Please replace paragraph [0019] with the following amended paragraph:

Referring back to Fig.2, a protrusion height of the dielectric layer 116 is smaller than a height of the spacers 124 so that the transmittance of the plasma panel 100 is not affected. In addition, the dielectric

layer 116 having the predefined pattern is usually formed by a screen printing method. However, the method for forming the dielectric layer 116 is not limited to this, other methods being able to achieve the same result, such as deposition followed by etching, may be utilized to form the dielectric layer 116 having the predefined pattern. Furthermore, recesses are formed in the dielectric layer 116 disposed on the top surface 108 of the rear plate 102, according to this preferred embodiment of the present invention, to increase the coating area for the fluorescent material, so as to increase the luminous efficiency of the plasma panel 100. In the present invention, the electrodes may be disposed on the bottom surface 118 of the front plate 104 and covered by a dielectric layer (not shown) having recesses to increase the coating area for the fluorescent material. Or the electrodes may be disposed on the bottom surface 118 of the front plate 104 or the top surface 108 of the rear plate 102, and the dielectric layer 116 having recesses are disposed on both the top surface 108 of the rear plate 102 and the bottom surface 118 of the front plate 104 (as shown in Fig. 5) to increase the coating area for the fluorescent material so as to increase the luminous efficiency of the plasma panel. In these two cases, the dielectric layer having a predefined pattern (116 ~~not shown~~) needs to be disposed on the bottom surface 118 of the front plate 104 before coating the fluorescent layer 122.

Please replace paragraph [0020] with the following amended paragraph:

Referring to Fig.4. Fig.4 is a top view of a plasma panel 200 according to a second preferred embodiment of the present invention. As shown in Fig.4, the present invention plasma panel 200 comprises a rear plate 202 and a front plate (not shown) disposed parallel with and spaced apart from the rear plate 202. Since the structure of the front plate (not shown) in this preferred embodiment of the present invention is the same as the structure of the front plate 102 in the first preferred embodiment of the present invention, it is not mentioned redundantly. A plurality of electrode pairs 206 are disposed on a top surface 208 of the rear plate 202. Each electrode pair 206 comprises a positive electrode 212 and a negative electrode 214. The positive electrode 212 and the negative electrode 214 of each electrode pair 206 are spaced equally, and a discharge gap is formed between the positive electrode 212 and the negative electrode 214 of each electrode pair 206.